terminal, as viewed in a sequence direction, at one end of the first sequence, and

a connector terminal at a second end of the second sequence extends to a position adjoining a connector terminal, as viewed in the sequence direction, at a second end of the first sequence.

2. 5. (Amended) The IC card according to claim 1, wherein said connector terminals include two ground voltage supply terminals, and one clock signal input terminal.

5. 13. (Amended) The IC card according to claim 1, wherein broad terminal-to-terminal distance is set to portions of a connector terminal of the first sequence facing the second sequence.

Please cancel Claim 12 without prejudice or disclaimer.

REMARKS

Favorable reconsideration of this application, as amended, is respectfully requested.

Claims 1-5 and 13 have been amended to clarify the invention intended to be claimed, and Claim 12 has been

cancelled in view of the amendment of Claim 1. The amendment of Claims 1 and 4 overcomes the claim objections.

A new abstract has been provided as required, and the suggested title has been adopted.

The claims now presented distinguish patentably from the prior art. Claims 1-3 recite, inter alia, that the first sequence includes a source voltage supply terminal and the second sequence is devoid of connector terminals at a position adjacent to the source voltage supply terminal and at positions adjacent to terminal-to-terminal areas at opposite sides of the source voltage supply terminal.

In Hamada (4,794,243), the principal reference relied upon in the rejections, if one of the terminals in the first sequence L1 were used as a source voltage supply terminal, the presence of near terminals in the adjacent sequence (L2) would create a short-circuit problem. See, e.g., Applicants' specification, paragraph bridging pages 40-41 and the first complete paragraph on page 41.

When connector terminals of a second sequence are laid out at positions adjacent to a source voltage supply terminal in a first sequence, a short-circuit of a power source can readily occur, because the distance between the source voltage supply terminal and socket terminals is very

short. Applicants' invention avoids this problem. The deficiencies of Hamada in this regard are not compensated by the secondary references, Berg et al., Okayama et al., and Murohara relied upon in the rejections.

Accordingly, independent Claims 1-3 and dependent Claims 4-7, and 13 should be allowed, and this application should be passed to issue.

The Examiner is authorized to cancel non-elected Claims 8-11 and 14-28 in order to pass this application to issue.

A marked-up copy of the amended claims and the abstract is attached.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been requested separately, such extension is hereby requested.

Respectfully submitted,

NHS:lmb

By: Nelson H. Shapiro

Nelson H. Shapiro
Reg. No. 17,095

Miles & Stockbridge P.C.
1751 Pinnacle Drive
Suite 500
McLean, Virginia 22102
(703) 903-9000
July 15, 2003

Marked-up copy of claims and Abstract- 09/756,867

1. (Amended) An [IC] integrated circuit (IC) card comprising:

a card substrate including,

a semiconductor integrated circuit chip mounted thereon; and

a plurality of connector terminals formed thereon;

said connector terminals being exposed from a casing;

wherein said connector terminals are laid out in [plural] first and second sequences [in] staggered relatively [form between the sequences] in a sequence direction and adjacent to one another forward and backward as viewed in an IC card inserting direction, and

wherein the first sequence includes a source voltage supply terminal, and the second sequence is devoid of connector terminals at a position adjacent to the source voltage supply terminal and at positions adjacent to terminal-to-terminal areas at opposite sides of the source voltage supply terminal.

2. (Amended) An [IC] integrated circuit (IC) card comprising:

a card substrate including,

a semiconductor integrated circuit chip mounted thereon; and

a plurality of connector terminals formed thereon;

said connector terminals being exposed from a casing;

wherein said connector terminals include an arrangement of [two] first and second sequences formed back and forth as viewed in an IC card inserting direction, and an arrangement of terminal-to-terminal areas [of connector terminals laid out] in [a] the first sequence and an arrangement of terminal-to-terminal areas [of connector terminals laid out] in [a] the second sequence are shifted from [each other] one another in a sequence direction, and

wherein the first sequence includes a source voltage supply terminal, and the second sequence is devoid of connector terminals at a position adjacent to the source voltage supply terminal and at positions adjacent to terminal-to-terminal areas at opposite sides of the source voltage supply terminal.

3. (Amended) An [IC] integrated circuit (IC) card comprising:

a card substrate including,

a semiconductor integrated circuit chip mounted thereon; and

a plurality of connector terminals formed thereon;

said connector terminals being exposed from a casing;

wherein said connector terminals include an arrangement of [two] first and second sequences formed back and forth as viewed in an IC card inserting direction, and [a sequence-directional layout of connector terminals laid out in a first sequence and a sequence-directional layout of connector terminals laid out in a second sequence are] shifted from [each other] one another in a sequence direction, and

wherein the first sequence includes a source voltage supply terminal, and the second sequence is devoid of connector terminals at a position adjacent to the source voltage supply terminal and at positions adjacent to terminal-to-terminal areas at opposite sides of the source voltage supply terminal.

4. (Amended) The IC card according to claim 3, wherein [the] a connector terminal at one end [extending in

a sequence direction, of the connector terminals laid out in] of the second sequence extends to a position [where said connector terminals adjoins the] adjoining a connector terminal, as viewed in a sequence direction, at one end [extending in the sequence direction,] of the [connector terminals laid out in the] first sequence, and

[the] a connector terminal at [the other] a second end of [extending in the sequence direction, of the connector terminals laid out in] the second sequence extends to a position [where said connector terminal adjoins the] adjoining a connector terminal, as viewed in the sequence direction, [at the other] at a second end [extending in the sequence direction,] of the [connector terminals laid out in the] first sequence.

5. (Amended) The IC card according to claim 1, wherein said connector terminals include [one source voltage supply terminal,] two ground voltage supply terminals, and one clock signal input terminal.

13. (Amended) The IC card according to claim 1, wherein [a connector terminal sequence corresponding to a first sequence as viewed in an IC card inserting direction

has a connector terminal in which] broad terminal-to-terminal distance is set to portions [where the] of a connector terminal of the first sequence facing [faces a connector terminal sequence corresponding to a] the second sequence.